15EC73

# Seventh Semester B.E. Degree Examination, July/August 2022 Power Electronics 

Time: 3 hrs .
Max. Marks: 80

## Note: Answer any FIVE full questions, choosing ONE full question from each module.

## Module-1

1 a. Explain five different types of power electronics converter circuits with their input and output wave forms.
(10 Marks)
b. A BJT switch has $\beta$ in the range of 8 to 40 , calculate (i) The value of $R_{B}$ that will result in saturation with an overdrive factor of 5 (ii) The forced beta $\beta_{\mathrm{f}}$ and the power loss in the transistor. $\mathrm{V}_{\mathrm{CC}}=200 \mathrm{~V}, \mathrm{~V}_{\mathrm{bb}}=10 \mathrm{~V}, \mathrm{~V}_{\mathrm{be}(\mathrm{sat})}=1.5 \mathrm{~V}, \mathrm{~V}_{\mathrm{ce}(\text { (sat) }}=1 \mathrm{~V}, \mathrm{R}_{\mathrm{C}}=10 \Omega$.
(06 Marks)

## OR

2 a. Explain any five power electronics devices with VI characteristics and their symbols.
(10 Marks)
b. An IGBT switch has $\mathrm{t}_{\mathrm{ON}}=3 \mu \mathrm{sec}, \mathrm{t}_{\mathrm{OFF}}=1.2 \mu \mathrm{sec}$, duty cycle $\mathrm{D}=0.7$,
$\mathrm{V}_{\mathrm{CE}(\mathrm{sat})}=2 \mathrm{~V}, \mathrm{f}_{\mathrm{s}}=1 \mathrm{kHz}$. Determine
(i) Average load current.
(ii) Conduction power loss.
(iii) Switching power loss during turn on and turn off.
(06 Marks)

## Module-2

3 a. With two transistor analogy, explain the working of a Thyristor and obtain the equation for anode current.
(08 Marks)
b. Distinguish between holding current and latching current of a Thyristor.
(02 Marks)
c. A SCR circuit operates from 300 V DC supply has series inductance of $4 \mu \mathrm{H}$. A resistance of $4 \Omega$ and capacitance of $0.2 \mu \mathrm{~F}$ is connected across the SCR. Calculate the safe $\frac{\mathrm{dv}}{\mathrm{dt}}$ and $\frac{\mathrm{di}}{\mathrm{dt}}$ ratings of SCR .
(06 Marks)

## OR

4 a. With necessary circuit diagram and waveforms explain the resonant pulse commutation.
(08 Marks)
b. The resonant pulse commutation circuit has a capacitance of $\mathrm{C}=30 \mu \mathrm{~F}$ and $\mathrm{L}=4 \mu \mathrm{H}$. The initial capacitor voltage is $\mathrm{V}_{0}=200 \mathrm{~V}$. Determine the circuit turn-off time for the load current $\mathrm{I}_{\mathrm{m}}=250 \mathrm{~A}$.
(08 Marks)

## Module-3

5 a. With neat circuit diagrams and suitable waveforms explain the working of a single phase dual converter.
(06 Marks)
b. Derive an expression for RMS output voltage of a single phase full controller having inductive load for discontinuous load current. Draw the relevant wave forms.
(06 Marks)
c. A single phase full wave ACVC has a resistive load of $\mathrm{R}=10 \Omega$ and the input voltage is $\mathrm{V}_{\mathrm{S}}=200 \mathrm{~V} \mathrm{rms} / 60 \mathrm{~Hz}$. The firing angles of $\mathrm{T}_{1}$ and $\mathrm{T}_{2}$ are $\alpha_{1}=\alpha_{2}=\frac{\pi}{2}$. Determine (i) RMS output voltage $V_{0}$ (ii) Input PF (iii) The average current of thyristors $I_{a}$. (iv) The rms current of the thyristor.
(04 Marks)

## OR

a. With the help of neat circuit diagram and wave forms explain the operation of single phase full wave bidirectional controller using diode bridge and single SCR with R load. Derive the equation for $\mathrm{V}_{\mathrm{O}}$ (RMS).
(10 Marks)
b. Obtain an expression for RMS value of load voltage in on-off A.C. voltage controller. For a $230 \mathrm{~V} / 50 \mathrm{~Hz}$ ON-OFF controller ON time is 10 cycles and off time is 4 cycles. Calculate $\mathrm{V}_{0(\text { RMS })}$ output voltage.
(06 Marks)

## Module-4

7 a. Explain the working of step-down choppers with necessary circuit diagram and was Derive the equation for $V_{O(a v)}$ and $V_{O R M S}$.
(06 Marks)
b. Give the classification of chopper. Explain briefly each one of them.

## OR

8 a. With the help of circuit diagram and waveforms, explain the operation of step-up chopper.
(06 Marks)
b. With a neat circuit diagram and wave forms explain the working principle of Buck regulator. Derive the expression for peak to peak ripple voltage of the capacitor, present across the load.
(10 Marks)

## Module-5

9 a. With circuit diagram and waveforms explain the working of a single phase full bridge inverter with RL load.
(10 Marks)
b. With neat circuit diagram, explain the variable DC link inverter.

## OR

10 a. With neat circuit diagram, explain the working of a transistorized current source inverter.
(06 Marks)
b. Explain the working of a solid state relay with suitable diagram.
(06 Marks)
c. Considering a single phase bridge inverter, if the DC voltage is 200 V and the required RMS fundamental output voltage is 90 V , determine the delay angle $\beta$.
(04 Marks)

